

REMARKS

Claims 5-7 are pending in the present application. Claim 5 has been amended to address the rejection under 35 U.S.C. § 112, second paragraph. Reconsideration of the rejection of the application is respectfully requested in view of the following remarks.

Rejections Under 35 U.S.C. § 112, Second Paragraph

Claim 5 was rejected under 35 U.S.C. § 112, second paragraph. Claim 5 has been amended as suggested by the Examiner. Reconsideration and withdrawal of the rejection of claim 5 under 35 U.S.C. § 112, second paragraph is respectfully requested.

The Claims Are Allowable Over the Prior Art

Claims 5-7 were rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. 6,429,684 to Houston ("Houston"). In the alternative, claims 5-7 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Houston.

Reference is made to Figs. 5A-D and Fig. 6 of Houston. The input signal IN shown in Fig. 6, is the gate signal for both of the left most transistors in the figure. The IN signal is also supplied to the gate inputs of a PMOS transistor and a NMOS transistor, which the current Office Action characterizes as the voltage detector of claim 5. The result is that the signal turns one of these transistors on and one of these transistors off. As described in Houston, the effect is that the source or drain of the transistor is coupled to the body of the transistor. (see, col. 5, lines 15-50 of Houston). As seen from the Abstract, this is the basic feature of the Houston reference.

Claim 5 recites that the gate terminal voltage is applied to the body of the transistor as a bias voltage (e.g., through series inverters as shown in Fig. 2A). At least this feature is neither shown nor suggested by Houston since the gate voltage is never supplied to the body of the transistor.

Since features of the pending claims are missing from the Houston reference, reconsideration and withdrawal of the rejection of claims 5-7 under 35 U.S.C. §§ 102(e) and/or 103(a) is respectfully requested.

CONCLUSION

Applicant respectfully requests entry of the above amendments and favorable action in connection with this application.

The Examiner is invited to contact the undersigned to discuss any matter concerning this application.

The Office is hereby authorized to charge any fees required under 37 C.F.R. §§ 1.16 or 1.17 or credit any overpayment to Kenyon and Kenyon Deposit Account No. 11-0600.

Respectfully submitted,

Date: June 7, 2006



Shawn W. O'Dowd
Registration No. 34,687
Attorney for Intel Corporation

KENYON & KENYON LLP
1500 K Street, N.W.
Suite 700
Washington, D.C. 20005
Ph.: (202) 220-4200
Fax.: (202) 220-4201
DC1-615378